

	Class	Subclass
ISSUE CLASSIFICATION		

PATENT NUMBER

U.S. UTILITY Patent Application

SCANNED CH(3) O.I.P.E. CL

PATENT DATE

APPLICATION NO. 09/542174	CONT/PRIOR	CLASS 365	SUBCLASS 70	ART UNIT 2133 2824	EXAMINER Dooley
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APPLICANTS

Hung-Mao Lin
Jyh-Cheng Lin
Douglas Chen

TITLE

Method and system for increasing yield in embedded memory devices

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL				CROSS REFERENCE(S)						
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION										

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ 	_____ (Primary Examiner) (Date)			ISSUE FEE 	
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